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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Airey et al.

Appl. No.: 09/614,363

Filed: July 12, 2000

For: **Display System Having Floating
Point Rasterization and Floating
Point Framebuffering**

Confirmation No.: 2211

Art Unit: 2672

Examiner: Havan, Thu-Thao

Atty. Docket: 15-4-632.51

(1452.3760001)

Letter to PTO Draftsman: Submission of Formal DrawingsCommissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450**RECEIVED**

DEC 30 2003

Technology Center 2600

Sir:

Submitted herewith are **seven (7)** sheets of formal drawings with **Figures 1-5**, corresponding to the informal drawings submitted with the above-captioned application. The formal drawings incorporate the drawing correction requested in the Submission of Proposed Drawing Amendment filed on July 12, 2000. Identification of the drawings is provided in accordance with 37 C.F.R. § 1.84(c). Acknowledgment of the receipt, approval, and entry of these formal drawings into this application is respectfully requested.

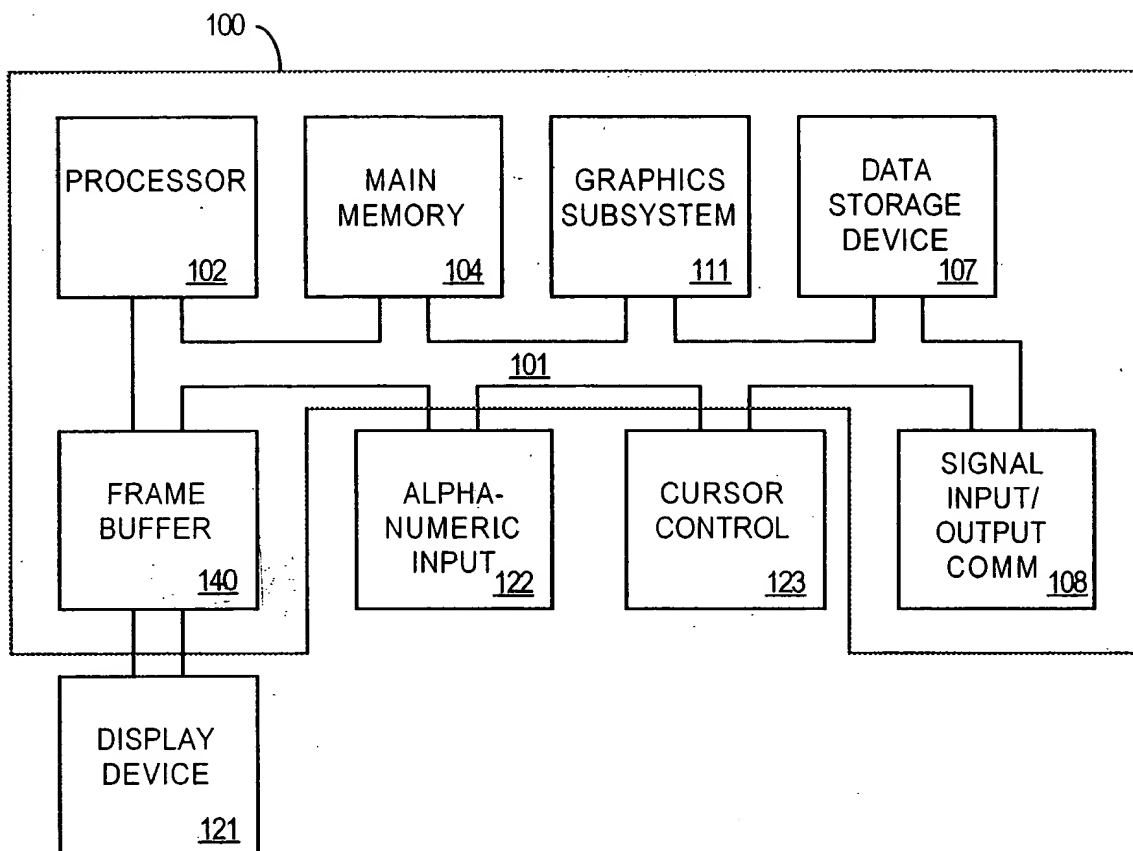
It is not believed that an extension of time is required, other than any already provided herewith. However, if an extension of time is needed to prevent abandonment of the application, then such extension of time is hereby petitioned. The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Michael V. Messinger
Attorney for Applicants
Registration No. 37,575Date: 12/23/031100 New York Avenue, N.W.
Washington, D.C. 20005-3934
(202) 371-2600

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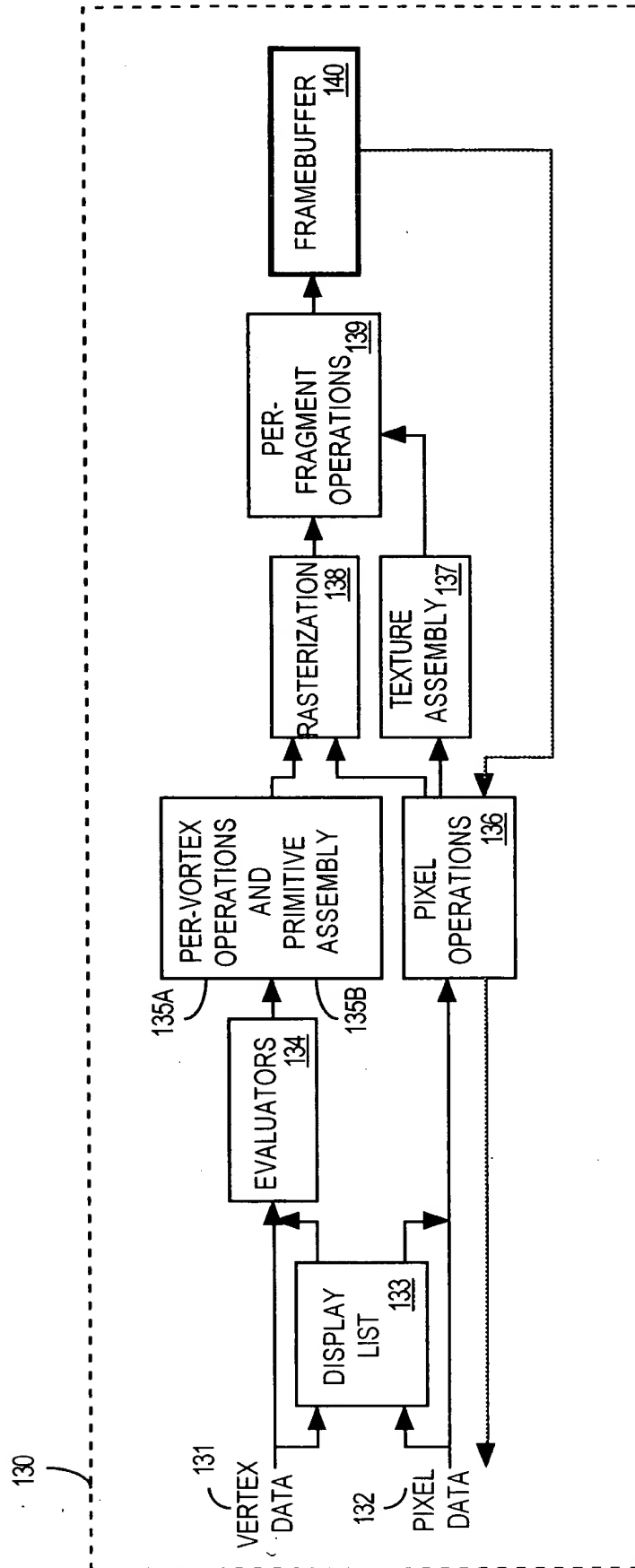


FIG. 2



VALUE	CONDITIONS**
$(-1)^s \times 2^{(e-16)} \times 1.m$	$00000 < e < 11111$
$(-1)^s \times 2^{15} \times 1.m$	$e == 11111, m! = 1111111111$
$(-1)^s \times 2^{-16} \times 1.m$	$e == 00000, m! = 0000000000$
zero	$e == 00000, s == 0, m == 0000000000$
NaN*	$e == 00000, s == 1, m == 0000000000$
positive infinity	$e == 11111, s == 0, m == 1111111111$
negative infinity	$e == 11111, s == 1, m == 1111111111$

* NaN: "Not a number," which is generated as the result of an invalid operation and also represents the concept of "negative zero"

** Extrapolation to s11e5 is readily achievable

FIG. 3



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Title: Display System Having Floating Point Rasterization
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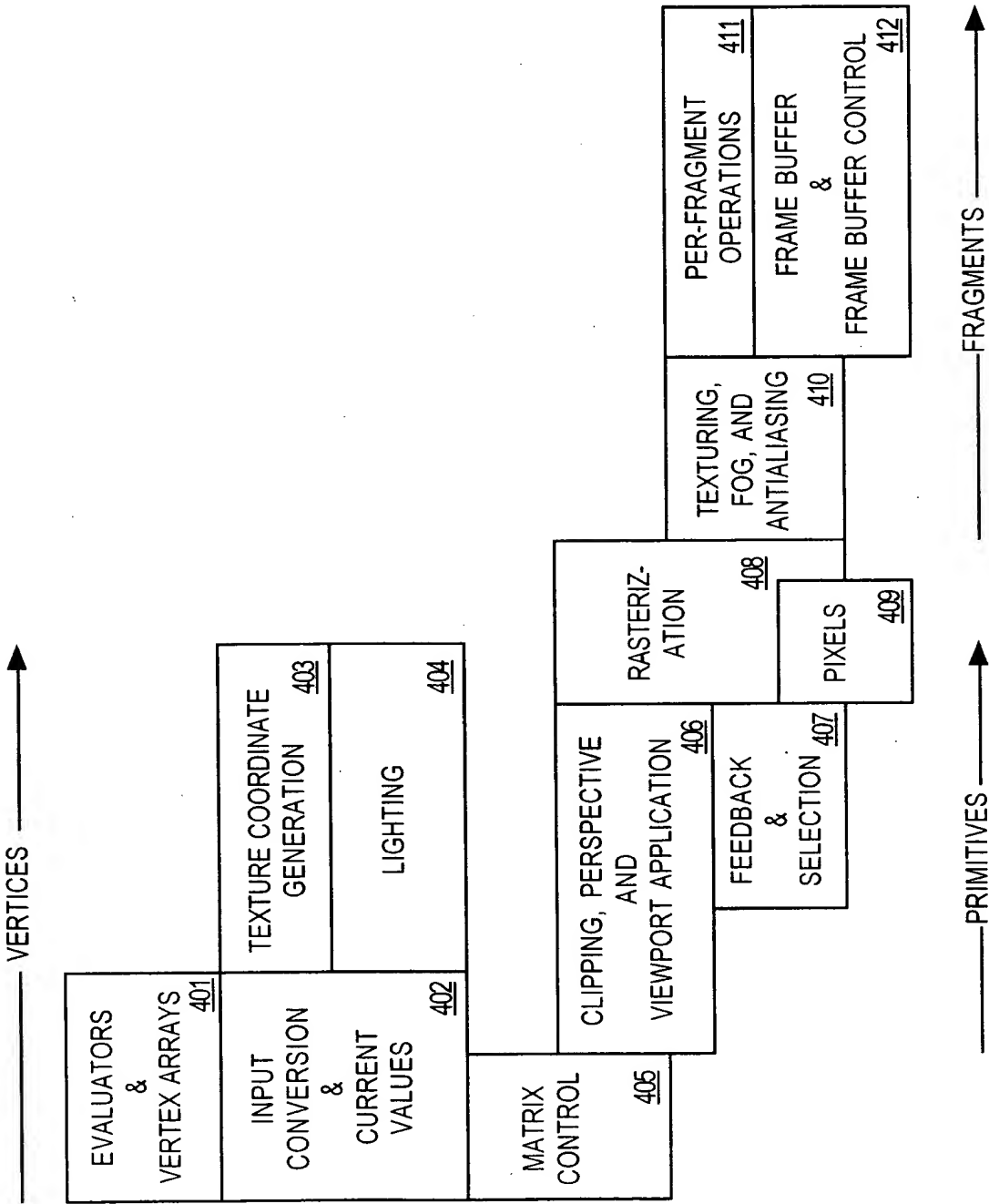


FIG. 4

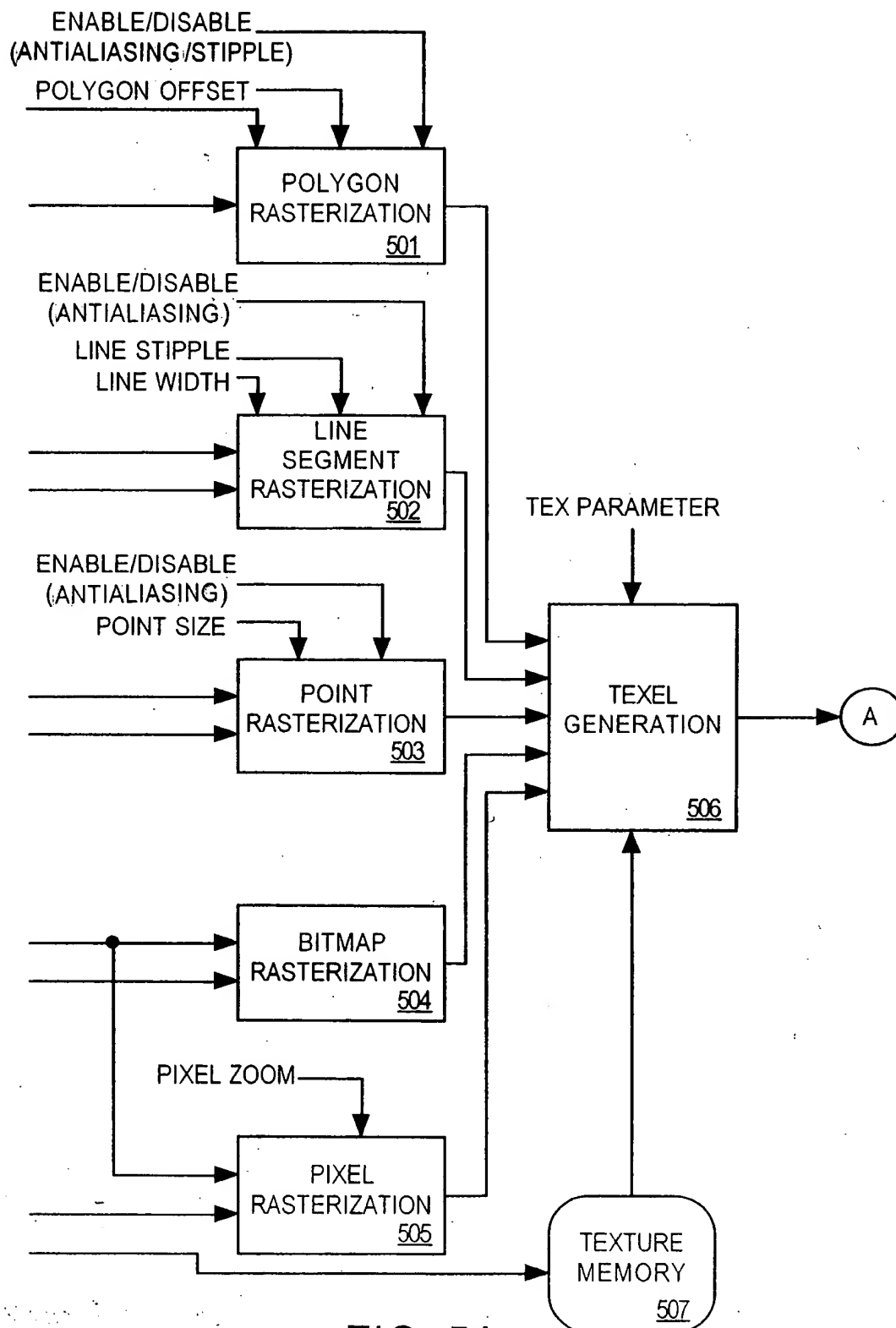


FIG. 5A



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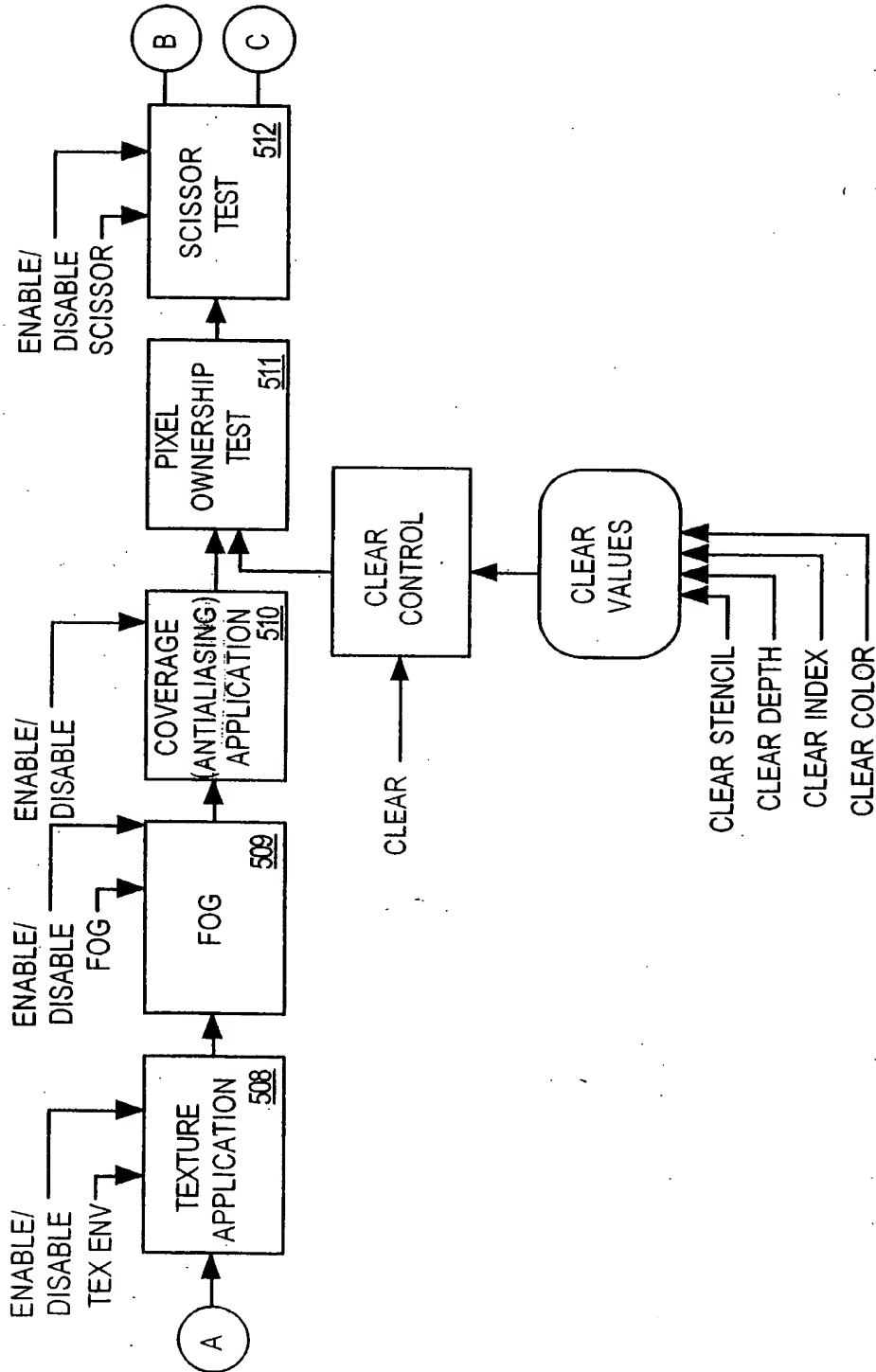


FIG. 5B

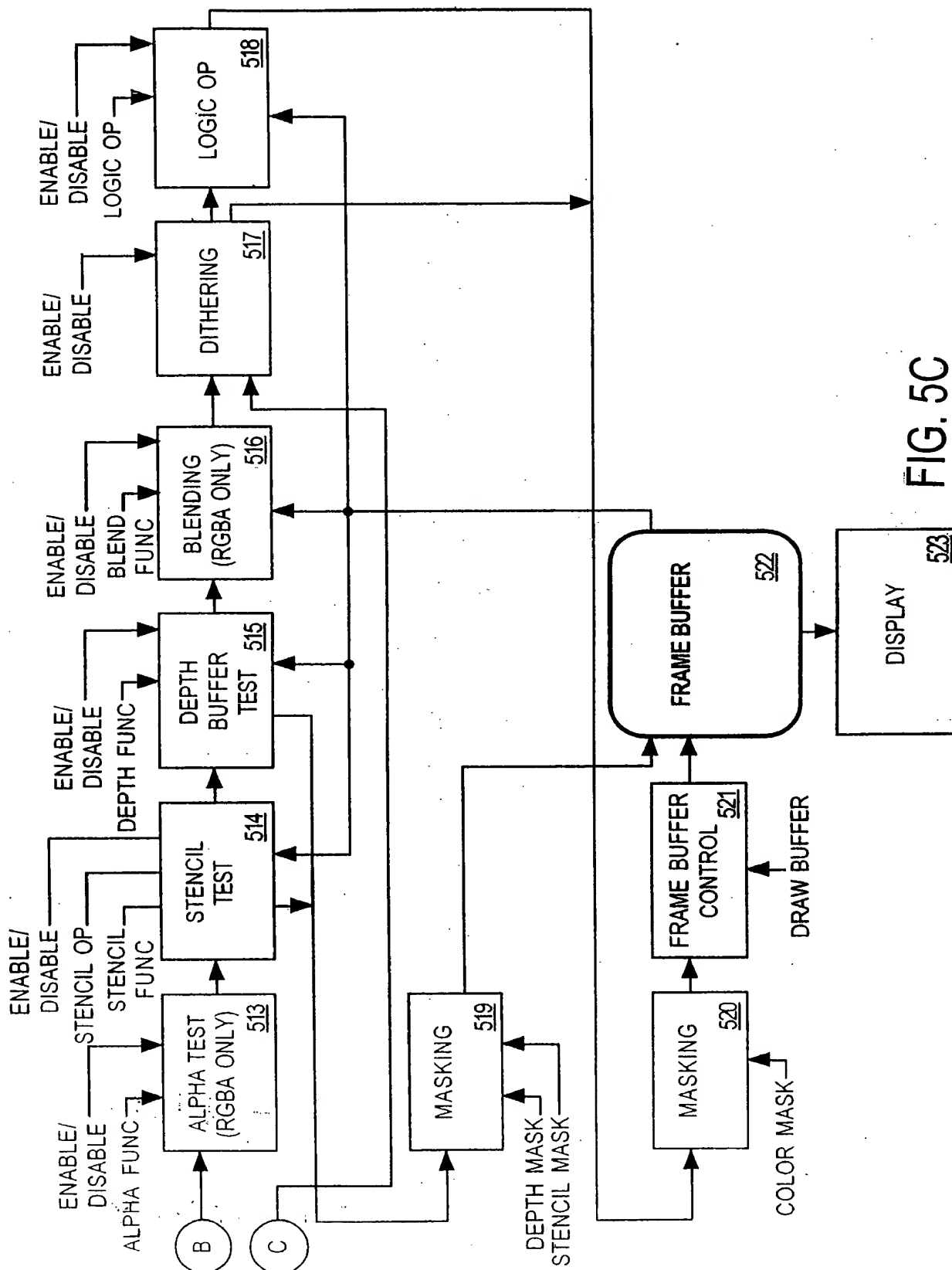


FIG. 5C